



2822 #

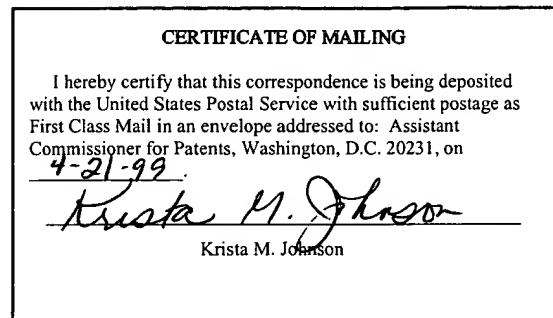
Docket: 0756-1630

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT Application of)
)
Shunpei YAMAZAKI)
)
Serial No.: 08/799,506)
)
Filed: February 12, 1997)
)
For: METHOD FOR PROCESSING)
SEMICONDUCTOR DEVICE,)
APPARATUS FOR PROCESSING)
A SEMICONDUCTOR AND)
APPARATUS FOR PROCESSING)
SEMICONDUCTOR DEVICE)

Art Unit: 2822

Examiner: Mike Lebentritt



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TRANSMITTAL LETTER

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Transmitted herewith is a Petition for Extension of Time, Amendment, Information Disclosure Statement and Identification of Co-Pending Applications in the above-identified application.

[X] A check in the amount of \$782.00 to cover the extension, IDS and extra claims fees is enclosed.

The fees have been calculated as shown on the next page:

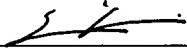
	Claims Remaining After Amendment		Highest Number Previously Paid For	Present Extra	Small/Large Entity Rate	Fee
Total	40	Minus	31	9	x \$9/\$18	\$162.00
Indep	8	Minus	8	0	x \$39/\$78	\$0
First Presentation of Multiple Dependent Claim					+ \$130/\$260	\$0
TOTAL						\$162.00

[X] In the event applicant(s) has overlooked the need for any petition and fee for extension of time, and such extension is required, applicant(s) requests that this be considered a petition therefor and that such fee be charged to Deposit Account No. 19-2380.

[X] The Commissioner is hereby authorized to charge fees under 37 CFR 1.16, 1.17, 1.20(a), 1.20(b), 1.20(c) and 1.20 (d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment, to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,

SIXBEY, FRIEDMAN, LEEDOM & FERGUSON, P.C.


Eric J. Robinson
Registration No. 38,285

EJR/LDE/kmj
Attorney Docket No.: 0756-1630

SIXBEY, FRIEDMAN, LEEDOM & FERGUSON, P.C.
8180 Greensboro Drive, Suite 800
McLean, Virginia 22102
Telephone (703) 790-9110

#2615
T. Young
53-99
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Group Art Unit: 2822

Shunpei YAMAZAKI

Examiner: Mike Lebentritt

Serial No.: 08/799,506

Filed: February 12, 1997

For: METHOD FOR PROCESSING
SEMICONDUCTOR DEVICE,
APPARATUS FOR PROCESSING
A SEMICONDUCTOR AND
APPARATUS FOR PROCESSING
SEMICONDUCTOR DEVICE



CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 4-21-99.

Krista M. Johnson
Krista M. Johnson

AMENDMENT

Repln. Ref: 05/19/1999 FCONNELL 00000004 08799506
DAH:192380 Name/Number: 08799506
FC: 704 Washington, D.C. 20231

Sir:

05/19/1999 FCONNELL 00000004 08799506

In response to the Office Action mailed December 3, 1998, please amend the above-
01 FC:103 18.00 OP

identified application as follows:

05/19/1999 FCONNELL 00000005 08799506

01 FC:199 108.00 OP
02 FC:199 18.00 OP

IN THE CLAIMS:

Please amend claims 8, 9, 12, 13, 21, 55, 56, 59 and 62-64 as follows:

Adjustment date: 05/19/1999 FCONNELL
04/27/1999 SARAYA 00000032 08799506
01 FC:103 162.00 OP

8. (Amended) An apparatus for processing a semiconductor on a substrate comprising:

a laser light generating means for generating a laser light wherein a cross section of said
laser light perpendicular to a length of said laser light has a width and a height, with said width
greater than said height;

04/27/1999 SARAYA 00000032 08799506

01 FC:103 162.00 OP

*OK refund 18.00
Jae.*

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